

E0545/1516P

PATENT

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Ghaemmaghami et al.

Serial No.: 09/497,320

Filed: February 3, 2000

Group Art Unit: 2815

Before the Examiner: Jose R. Diaz

Title: METHOD AND SYSTEM FOR PROVIDING HALO IMPLANT TO A SEMICONDUCTOR DEVICE WITH MINIMAL IMPACT TO THE JUNCTION CAPACITANCE

REPLY UNDER 37 C.F.R. § 1.111


Assistant Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

In response to the Office Action having a mailing date of November 19, 2002, having a three-month shortened statutory period for response set to expire on February 19, 2003, please reconsider the rejection of the claims in view of the following remarks:

CERTIFICATION UNDER 37 C.F.R. § 1.8

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Assistant Commissioner for Patents, Washington, D.C. 20231, on February 3, 2003.


Signature

Serena Beller
(Printed name of person certifying)

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